

**WHAT IS CLAIMED IS:**

1. An image signal processing system comprising:

an image sensor for receiving an image of a subject in a light form

5 under the control of a shutter control signal to generate analog signals;

a variable gain amplifier for amplifying output signals of the image sensor under the control of a gain control signal to maximize dispersion of the analog signals;

10 a first A/D converter for receiving the output signals of the variable gain amplifier and converting the received output signals into digital signals;

a second A/D converter for receiving the output signals of the image sensor and converting the received output signals into the digital signals; and

15 an image data processor for receiving the output signals of the first A/D converter and the output signals of the second A/D converter to find a movement value and generating the gain control signal and the shutter control signal.

2. The image signal processing system as claimed in claim 1, wherein

the shutter control signal is generated by using the output signals of the second

20 A/D converter.

3. The image signal processing system as claimed in claim 1, wherein

the variable gain amplifier is a sample-and-hold amplifier architecture.

4. The image signal processing system as claimed in claim 1, wherein the second A/D converter is configured of a plurality of analog comparators.

5. An image signal processing system comprising:

5 an image sensor for receiving an image of a subject in a light form under the control of a shutter control signal to generate analog signals;

a direct current offset controller for controlling direct current offsets of output signals of the image sensor under the control of an offset control signal;

a variable gain amplifier for amplifying output signals of the direct  
10 current offset controller under the control of a gain control signal to maximize dispersion of the output signals;

a first A/D converter for receiving the output signals of the variable gain amplifier and converting the received output signals into digital signals;

a second A/D converter for receiving the output signals of the image  
15 sensor and converting the received output signals into the digital signals; and

an image data processor for receiving the output signals of the first A/D converter and the output signals of the second A/D converter to find a movement value and generating the gain control signal, the offset control signal and the shutter control signal.

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6. The image signal processing system as claimed in claim 5, wherein the shutter control signal is generated by the output signals of the second A/D converter.

7. The image signal processing system as claimed in claim 5, wherein the variable gain amplifier is a sample-and-hold amplifier architecture.

8. The image signal processing system as claimed in claim 5, wherein  
5 the second A/D converter is configured of a plurality of analog comparators.